

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Gopalakrishnan <i>et al.</i>	Examiner:	Salerno, Sarah Kate
Serial No.:	10/518,779	Group Art Unit:	2814
Filed:	June 6, 2005	Docket No.:	STFD.035US (S02-114US)
Title:	INSULATED-GATE SEMICONDUCTOR DEVICE AND APPROACH INVOLVING JUNCTION-INDUCED INTERMEDIATE REGION		

MAIL STOP RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Customer No.

40581

1. This is a **Request for Continued Examination (RCE)** under 37 C.F.R. §1.114 of the above-identified patent application. This request for continued examination is filed within three months of the Final Office Action dated September 21, 2009; therefore an extension fee should not be required.
2. The following are submitted under 37 C.F.R. § 1.114, to further advance prosecution of the present application for reexamination:

☒ An Amendment and Response with RCE; which provides a complete listing of the claims, to include amendments thereto.


3. Fees:

☒ Please charge Deposit Account Number 50-0996 (STFD.035US) the below RCE filing fee, with authorization to charge/credit said deposit account any required claims fees/overages to complete this filing.

The RCE Small-Entity filing fee of \$405.00.

Entry of this Request for Continued Examination, and a favorable reply, are earnestly requested.

Respectfully Submitted,

By: 
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Dated: December 18, 2009

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